



Jordan University of Science and Technology
Faculty of Computer & Information Technology
Computer Engineering Department

CPE232 Digital Logic Design Lab

Summer Semester 2019-2020

Course Catalog

1 Credit Hours.

Class Schedule & Room

Prerequisites

Line Number	Course Name	Prerequisite Type
1712310	CPE231 Digital Logic Design	Prerequisite / Study

Relationship to Program Student Outcomes (Out of 100%)

A	B	C	D	E	F	G	H	I	J	K	SO1	SO2	SO3	SO4	SO5	SO6	SO7

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