Resume

Dr. Rami Abdallah AL na'mneh
Computer Engineering Department
Jordan University of Science and Technology
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Personal information

Nationality: Jordanian
Date of Birth: August 8, 1977
Marital Status: Married

Education

B.S. in Electrical and Computer Engineering: Sept. 1995 - June 2000
 Jordan University of Science and Technology
M.S. in Computer Engineering: Sept. 2001 - June 2003
The University of Alabama in Huntsville
Ph.D. in Computer Engineering: Sept. 2003 - August 2006
The University of Alabama in Huntsville

Experience

2000-2001 teaching and research assistant in Jordan University of Science and Technology
2003-2006 teaching assistant in the University of Alabama in Huntsville
2006-2009 assistant professor in Jordan university of Science and Technology

Research areas

Parallel algorithms, parallel programming

Teaching (course taught)

CPE 251	Digital Logic Design	Fall 2007
CS 310	Software engineering	Fall 2007
CPE 253/255	Digital Logic Design	Spring 2007

	Lab	Spring 2009
CPE 380	Project management	Fall 2008 Spring 2008 Summer 2008
CPE 312	Numerical analysis	Fall 2009
CS 115	C++	Fall 2009 Spring 2009 Summer 2009
CPE 595	Parallel algorithms	Spring 2008

Sample Publications

1. R. A. Al Na'mneh, and W. Pan, "Performance Analysis of Parallel Algorithms for One Dimension FFT on Symmetric Multiprocessors," *Proc. of the International Conference on Parallel and Distributed Processing Techniques and Application*s (PDPTA'04), pp. 80-83, Las Vegas, Nevada, June 2004.
2. R. A. Al Na'mneh, and W. Pan, "Efficient and Scalable Parallel Processing Methods for Transposing Matrices on Symmetric Multiprocessors," *Proc. of the International Conference on Parallel and Distributed Processing Techniques and Applications* (PDPTA'04), pp. 84-89, Las Vegas, Neva
3. R. Al Na'mneh, W. Pan, and R. Adhami, "Parallel Implementation of 1-D Fast Fourier Transform without Inter-processor Communication," *Proc. of IEEE Southeastern Symposium on System Theory*, Tuskegee, Alabama, March 2005.
4. Al Na'mneh, W. Pan, and R. Adhami, "Communication Efficient Adaptive Matrix Transpose Algorithms for FFT on Symmetric Multiprocessors," *Proc. of IEEE Southeastern Symposium on System Theory*, Tuskegee, Alabama, March 2005
5. R. A. Al-Na'mneh and W. D. Pan, "Five-Step FFT Algorithm with Reduced Computational Complexity," *Information Processing Letters*, vol. 101, no. 6, pp. 262-267, March 2007.
6. R. A. AL-Na'mneh, W. D. Pan, and S.-M. Yoo, "Parallel Implementation of 1-D Fast Fourier Transform without Inter-Processor Communication," *International Journal of Computers and Applications*, vol. 29, no. 2, 2007
7. R. A. Al-Na'mneh, W. D. Pan, and S.-M. Yoo, "Efficient Adaptive Algorithm for Transposing Small and Large Matrices on Symmetric Multiprocessors," *International Journal Informatica*, vol. 17, no. 4, pp. 535-550, 2006.
8. R. A. Al-Na'mneh, W. D. Pan, and S.-M. Yoo, "Two Parallel 1-D FFT algorithms without All-to-All Communication," *Parallel Processing Letters*, vol. 16, no. 2, pp. 153-164, June 2006.